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canceled.

80. The microprocessor as recited in claim 78 wherein the branch instruction is a call instruction, and wherein the execution unit is configured to store a return address in response to executing the call instruction. --

- 5 81. The microprocessor as recited in claim 78 wherein the execution unit is further configured to execute a return instruction within said second routine, the return instruction indicating the return address.

REMARKS

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Claims 1 - 41 are pending in the application. Claims 1 - 41 have been cancelled. Claims 42 - 81 have been added. Claims 42 - 81 accordingly remain pending in the application.

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A Fee Authorization form is enclosed; however, the Commissioner is hereby authorized to charge any additional fees which may be required to Deposit Account No. 12-2252/5201-02906/P2920-1D/BNK.

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Respectfully submitted,



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